

TENTATIVE LESSON PLAN FOR SESSION :2018-19

NAME OF EMPLOYEE: GUEST FACULTY
 DEPARTMENT:COMPUTER ENGINEERING
 DESIGNATION: LECTURER
 SUB: Microprocessors and Peripheral Device
 TEACHING LOAD:4(L)-3(p)
 SEMESTER:4TH

WEEK NO.	THEORY DAY	TOPICS COVERED	PRACTICAL DAY	PRACTICAL DONE
1	1	Introduction of microprocessor		
	2	Functions of its blocks	1st	Introduction to microprocesor kit.
	3	Function and impact on modern society and its uses		
	4	Microprocessors and Computers applications		
2	1	Introduction of Architecture of a microprocessor		Familiarization of different key of 8085 Microprocessor kit and its memory map
	2	Concept of bus organization of 8085,	1st	
	3	Functional block diagram of 8085		
	4	Revision and Class -test		
3	1	Function of each block pin details of 8085 and		
	2	Step to execute a stored programme	1st	Steps to enter modify data/program and to execute a programme on 8085 kit
	3	Demultiplexing of Address/data		
	4	Revision and Class -test		
4	1	Instruction timing and cycles		
	2	Machine cycle and t-states	1st	Writing and execution of ALP for Addition and subtraction of two 8 bit numbers
	3	Fetch and execute cycle		
	4	Revision and Class test		
5	1	Introduction to programming		
	2	Brief idea of machine and assembly languages,	1st	Writing and execution of ALP for multiplication and division of two 8 bit
	3	Machine and mnemonic codes.		
	4	Revision and Class -test		
6	1	Instruction format and Addressing mode.		
	2	Identification of instructions	1st	Writing and execution of ALP for arranging 10 number in
	3	Concept of instruction set		
	4	Revision and Class -test		
7	1	Data transfer group ,Arithmetic Group ,logic Group		ascending /descending order
	2	Stack O/I and Machine control group		
	3	Programming exercise in assembly language.		
	4	Revision and Class -test		
8	1	Introduction of Memories and I/O interfacing		Writing and execution of ALP for 0 to 9 BCD
	2	Concept of memory mapping	1st	
	3	Partitioning of total memory space		
	4	Revision and Class -test		
9	1	Address decoding concept of peripheral mapped I/O		
	2	Interfacing of memory mapped I/O devices	1st	Interfacing exercise on 8255 like LED Display control
	3	Concept of interrupt		
	4	Revision and Class -test		
10	1	Introduction of Interrupts		
	2	Maskable and non maskable Edge triggered interrupts.	1st	
	3	Software interrupt ,restart interrupts and its use		
	4	Revision and Class -test		
11	1	Various Hardware interrupts of 8085		
	2	Servicing interrupts extending interrupt system		
	3	Software interrupt		
	4	class test		
12	1	Introduction to data transfer techniques		
	2	Concept of programmed I/O Operations		
	3	Sync data transfer, async data transfer (hand shaking)		
	4	Revision and Class -test		
13	1	Interrupt driven data transfer DMA	1st	Interfacing exercise on 8253
	2	Serial output data Serial input data		Programmable interval timer
	3	Peripheral devices		

	4	Revision and Class -test		
14	1	Introduction of peripheral deices		Interfacing exercise on 8279 programmable
	2	8255PPI and 8253PIT 8257	1st	KB/display interface like to display the code
	3	DMA controller		
	4	Revision and Class -test		
15	1	8279 programmable KG/Display		
	2	Interface 8251 communication interface adapter	1st	Use of 8085 emulator for hardware testing
	3	interfaseses in detail		
	4	Revision and Class -test		