

## Lesson Plan

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**Discipline** :- **Electrical Engineering**  
**Semester** :- **Fourth Semester**  
**Subject** :- **Electronics -II**

**Lesson Plan Duration:- 15 Week**

Week	Theory		Practical	
	Lecture Day	Topic	Practical Day	Topic
1 <sup>st</sup>	1 <sup>st</sup>	Transistor Audio Power Amplifier Difference between voltage and power amplifier	1 <sup>st</sup>	To study the effect of coupling capacitor on lower cut off frequency and upper cut off frequency by plotting frequency response curve of a two stage RC coupled amplifier
	2 <sup>nd</sup>	Important terms in Power Amplifier, collector efficiency, distortion and dissipation capability		
	3 <sup>rd</sup>	Classification of power amplifier class A, B and C	2 <sup>nd</sup>	-- do--
	4 <sup>th</sup>	Class A single-ended power amplifier, its working and collector efficiency		
	1 <sup>st</sup>	Impedance matching in a power amplifier using transformer	1 <sup>st</sup>	To measure (a) optimum load (b) output power (c) signal handling capacity of a push-pull amplifier
	2 <sup>nd</sup>	Heat sinks in power amplifiers Push-pull amplifier: circuit		

2 <sup>nd</sup>		details, working and advantages (no mathematical derivations)		
	3 <sup>rd</sup>	REVISION	2 <sup>nd</sup>	-- do--
	4 <sup>th</sup>	REVISION		
3 <sup>rd</sup>	1 <sup>st</sup>	Principles of the working of complementary symmetry push-pull amplifier		To observe the effect of negative current feedback on the voltage gain of a single stage transistor amplifier by removing emitter by-pass capacitor.
	2 <sup>nd</sup>	Tuned Voltage Amplifier Introduction		
	3 <sup>rd</sup>	Series and parallel resonance (No mathematical derivation)	2 <sup>nd</sup>	-- do--
	4 <sup>th</sup>	Single and double tuned voltage amplifiers		
4 <sup>th</sup>	1 <sup>st</sup>	Frequency response of tuned voltage amplifiers Applications of tuned voltage amplifiers	1 <sup>st</sup>	To measure (a) voltage gain (b) input and output impedance for an emitter follower circuit
	2 <sup>nd</sup>	Feedback in Amplifiers Feedback and its importance, positive and negative feedback and their need		
	3 <sup>rd</sup>	REVISION	2 <sup>nd</sup>	-- do--
	4 <sup>th</sup>	REVISION		
5 <sup>th</sup>	1 <sup>st</sup>	Voltage gain of an amplifier with negative feedback $A = \frac{\dots}{1+\beta A}$	1 <sup>st</sup>	To measure frequency generation in (a) Hartley (b) R-C Phase Shift oscillator
	2 <sup>nd</sup>	Effect of negative feedback on voltage gain, stability, distortion, band width, output and input impedance of an amplifier (No mathematical derivation)		
	3 <sup>rd</sup>	REVISION	2 <sup>nd</sup>	-- do--
	4 <sup>th</sup>	REVISION		

6 <sup>th</sup>	1 <sup>st</sup>	Typical feedback circuits Effect of removing the emitter bypass capacitor on a CE transistor amplifier	1 <sup>st</sup>	To observe the differentiated and integrated square wave on a CRO for different values of R-C time constant
	2 <sup>nd</sup>	Emitter follower and its applications		
	3 <sup>rd</sup>	Sinusoidal Oscillators 4.1. Sinusoidal Oscillators – positive feedback in amplifiers 4.2. Difference between an oscillator and an alternator	2 <sup>nd</sup>	
	4 <sup>th</sup>	REVISION		
7 <sup>th</sup>	1 <sup>st</sup>	Essentials of an oscillator	1 <sup>st</sup>	Clipping of both portion of sine-wave using: a) diode and dc source b) zener diodes
	2 <sup>nd</sup>	Circuit details and working of LC oscillators viz. Tuned Collector, Hartley and Colpitt's oscillators		
	3 <sup>rd</sup>	REVISION	2 <sup>nd</sup>	-- do--
	4 <sup>th</sup>	REVISION		
8 <sup>th</sup>	1 <sup>st</sup>	R-C oscillator circuits, phase shift oscillators	1 <sup>st</sup>	Clamping a sine-wave to: a) Negative dc voltage b) Positive dc voltage
	2 <sup>nd</sup>	Wein bridge oscillator circuits		
	3 <sup>rd</sup>	Introduction to piezoelectric crystal and crystal oscillator circuit	2 <sup>nd</sup>	-- do--
	4 <sup>th</sup>	REVISION		
9 <sup>th</sup>	1 <sup>st</sup>	Wave-Shaping and Switching Circuits Concept of Wave-shaping	1 <sup>st</sup>	To generate square-wave using an astable multivibrator and to observe the wave form on a CRO and verify the result using p-spice software
	2 <sup>nd</sup>	Wave-shaping circuits a. R-C differentiating and integrating circuits b. Diode clipping circuits		
	3 <sup>rd</sup>	c. Diode clamping circuits	2 <sup>nd</sup>	-- do--
	4 <sup>th</sup>	REVISION		

10 <sup>th</sup>	1 <sup>st</sup>	d. Applications of wave-shaping circuits	1 <sup>st</sup>	To observe triggering and working of a bistable multivibrator circuit and observe its output wave form on a CRO
	2 <sup>nd</sup>	Transistor as a switch - explanation using CE transistor characteristics		
	3 <sup>rd</sup>	Collector coupled astable, monostable multivibrator circuits. (explanation using wave shapes).	2 <sup>nd</sup>	-- do--
	4 <sup>th</sup>	REVISION		
11 <sup>th</sup>	1 <sup>st</sup>	Collector coupled bistable multivibrator circuits (explanation using wave shapes). Brief mention of uses of multivibrators	1 <sup>st</sup>	To use the op-Amp (IC 741) as inverting one and non-inverting amplifiers, adder, comparator, integrator and differentiator and verify the result using p-spice software
	2 <sup>nd</sup>	Working and applications of transistor inverter circuit using power transistors		
	3 <sup>rd</sup>	REVISION	2 <sup>nd</sup>	-- do--
	4 <sup>th</sup>	REVISION		
12 <sup>th</sup>	1 <sup>st</sup>	Power supplies - Working Principles of different types of power supplies viz. CVTs, IC voltage regulator (78 XX,79XX)	1 <sup>st</sup>	To study the pin configuration and working of IC 555 and its use as monostable and astable multivibrator
	2 <sup>nd</sup>	Operational Amplifier The basic operational amplifier. The differential amplifier. The emitter coupled differential amplifier. Offset even voltages and currents		
	3 <sup>rd</sup>	REVISION	2 <sup>nd</sup>	-- do--
	4 <sup>th</sup>	REVISION		
13 <sup>th</sup>	1 <sup>st</sup>	Basic operational amplifier applications, integrator and differentiator	1 <sup>st</sup>	To realize the regulated power supply by using three terminal voltage regulator ICs such as 7805, 7905, 7915 etc. and verify the result using p-spice software.
	2 <sup>nd</sup>	summer, subtractor		

	3 <sup>rd</sup>	REVISION	2 <sup>nd</sup>	REVISION PRACTICAL
	4 <sup>th</sup>	REVISION		
14 <sup>th</sup>	1 <sup>st</sup>	Familiarization with specifications and pin configuration of IC 741 Block diagram and operation of 555 IC timer	1 <sup>st</sup>	REVISION PRACTICAL
	2 <sup>nd</sup>	REVISION		
	3 <sup>rd</sup>	REVISION	2 <sup>nd</sup>	REVISION PRACTICAL
	4 <sup>th</sup>	REVISION		
15 <sup>th</sup>	1 <sup>st</sup>	REVISION	1 <sup>st</sup>	REVISION PRACTICAL
	2 <sup>nd</sup>	REVISION		
	3 <sup>rd</sup>	REVISION	2 <sup>nd</sup>	REVISION PRACTICAL
	4 <sup>th</sup>	REVISION		